

AMENDMENTS TO THE SPECIFICATION:

Please replace paragraph [0010] beginning on page 4 with the following rewritten version:

-- Referring to FIG. 2, the mixer circuit includes three pairs of transistors 11, 12, 15, 16, 17 and 18, first and second current sources 10 and 22. The first pair of transistors 11 and 12 controls current flowing between their collectors and emitters according to a first input signal applied differentially to first input terminals I/P1+ and I/P1-. The two second pairs of transistors 15, 16, 17 and 18 control the quantity of currents flowing through them according to a second input signal applied to second input terminals I/P+ and I/P- and the current of a corresponding transistor of the first pair of transistors. The second current source 10 provides for the total current passed by the first pair of transistors 11 and 12 to be greater than the total current passed by the two second transistors 15, 16, 17 and 18. --

Please replace paragraph [0050] beginning on page 15 with the following rewritten version:

-- The first current source I31 provides a specific quantity of current to the third input terminal 309 of the mixing unit 330 to allow the mixer circuit to operate in optimum state. In this case, the quantity of current provided by the first current source I31 and amplification unit 310 is identical to the sum of currents I_c passed by the collectors of the second and third NPN transistors BN32 and BN33 of the mixing unit 330 and larger than the quantity of current passed by the first NPN transistor BN31 of the amplification unit 310. --